

**AMENDMENTS TO THE CLAIMS**

**This listing of claims will replace all prior versions and listings of claims in the application:**

**LISTING OF CLAIMS:**

1. (original): A dummy wafer formed by sintering a mixture containing a silicon carbide powder and a non-metallic sintering auxiliary, wherein

a coating film layer containing silicon carbide is provided on the surface of the dummy wafer including at least one of either upper and lower main faces of the dummy wafer by the chemical vapor deposition method.

2. (original): The dummy wafer according to claim 1, wherein the coating film layer containing silicon carbide is provided on the whole perimeter of the surface of the dummy wafer including the side surface of the dummy wafer.

3. (currently amended): The dummy wafer according to claim 1 ~~or~~ 2, wherein the coating film layer has a thickness of 20  $\mu\text{m}$  or more and 70  $\mu\text{m}$  or less, and a surface roughness (Ra) of 10 nm or less.

4. (original): A method for manufacturing a dummy wafer formed by sintering a mixture containing a silicon carbide powder and a non-metallic sintering auxiliary, wherein the method for manufacturing a dummy wafer has a step of providing a coating film layer containing silicon carbide with a coating film thickness of 20  $\mu\text{m}$  or more and 70  $\mu\text{m}$  or less on

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the surface of the dummy wafer including at least one of either upper and lower main faces of the dummy wafer by the chemical vapor deposition method.

5. (original): The method for manufacturing a dummy wafer according to claim 4, wherein the coating film thickness of the coating film layer is 20  $\mu\text{m}$  or more and 40  $\mu\text{m}$  or less.

6. (currently amended): The method for manufacturing a dummy wafer according to claim 4 ~~or 5~~, further having a step of polishing the surface of the coating film layer.

7. (original): The method for manufacturing a dummy wafer according to claim 6, wherein the coating film layer after polishing the surface has a thickness of 20  $\mu\text{m}$  or more and 70  $\mu\text{m}$  or less, and a surface roughness (Ra) of 10 nm or less.

8. (currently amended): The dummy wafer according to ~~any one of claims 1 to 3~~ claim 1, wherein the dummy wafer is for a monitor wafer.